

## **AMENDMENTS**

### ***In the Specification***

Please replace the paragraph beginning on Page 5, line 1 with the following amended paragraph:

Referring to Figure 2, one of the modes in which the SRAM controller 114 functions is as a PCMCIA interface in which the SRAM controller 114 provides a PCMCIA interface. The PCMCIA interface may be used by the SRAM controller 114 to provide an IDE interface. More specifically, when the SRAM controller 114 is configured for the PCMCIA mode, as ~~indicted~~ indicated via the static bus chip select signal, the signals set forth in Figure 2 are used to interface to an IDE device via the IDE connector 182.